vmeambt

WR\I
VMEADD<23..1>\I
MRESET\I
LWORD_\I
IACK_\I
GEADD\I
DV_UP\I
DS1_\I
DS0_\I
XTCLK\I
AS_\I
AM<5..0>\I

WRITE
VMEADD23
VMEADD22
VMEADD21
VMEADD20
VMEADD19
VMEADD18
VMEADD17
VMEADD16
VMEADD15
VMEADD14
VMEADD13
VMEADD12
VMEADD11
VMEADD10
VMEADD9
VMEADD8
VMEADD7
VMEADD6
VMEADD5
VMEADD4
VMEADD3
VMEADD2
VMEADD1
MRESET
LWORD_
IACK_
GEADD
DV_UP
DS1_
DS0_
CLOCK
AS_
AM5
AM4
AM3
AM2
AM1
AM0

20P (MSB / LSB)
22P (MSB / LSB)
16P (MSB / LSB)
21P (MSB / LSB)
15P (MSB / LSB)

55P
PART TYPE=73144P0160-10
FILE=VMEAMBT

P34 1 PAD1 ST1 54P
P35 1 PAD1 ST2 53P
P42 1 PAD1 ST3 52P
P69 1 PAD1 ST4 51P
P72 1 PAD1 ST5 50P
P73 1 PAD1 ST6 49P
P75 1 PAD1 ST7 48P
P92 1 PAD1 ST8 47P
P95 1 PAD1 ST9 46P
P97 1 PAD1 ST10 45P
P98 1 PAD1 ST11 44P
P101 1 PAD1 ST12 43P
P111 1 PAD1 ST13 42P
P116 1 PAD1 ST14 41P
P117 1 PAD1 ST15 40P
P122 1 PAD1 ST16 39P
P124 1 PAD1 ST17 38P
P129 1 PAD1 ST18 37P
P132 1 PAD1 ST19 36P
P133 1 PAD1 ST20 35P
P140 1 PAD1 ST21 34P
P145 1 PAD1 ST22 33P

TMODE — TMODE\I
SWITCH — SWTCH\I
SV5 — SV5
SV4 — SV4
SV3 — SV3
SV2 — SV2
SV1 — SV1
SV0 — SV0
SEL_UP — SEL_UP\I
RESET — RESET\I

PROMADD15
PROMADD14
PROMADD13
PROMADD12
PROMADD11
PROMADD10
PROMADD9
PROMADD8
PROMADD7
PROMADD6
PROMADD5
PROMADD4
PROMADD3
PROMADD2
PROMADD1
PROMADD0
56P (MSB / LSB)
3P (MSB / LSB)
2P PROMADD<15..0>\I

OUTDVUP — OUTDV\I
OUTADDR13
OUTADDR12
OUTADDR11
OUTADDR10
OUTADDR9
OUTADDR8
OUTADDR7
OUTADDR6
OUTADDR5
OUTADDR4
OUTADDR3
OUTADDR2
OUTADDR1
OUTADDR0
25P (MSB / LSB)
24P (MSB / LSB)
23P ADD_UP<13..0>\I

OPCODE_UP3
OPCODE_UP2
OPCODE_UP1
OPCODE_UP0
1P (MSB / LSB)
OPC<3..0>\I

OEP3 — OEP3\I
OEPROM — OEPROM\I
OEGLUE — OEGLUE
OEBA — OEBA\I
OEADD — OEADD
OEAB — OEAB\I

MODE1
MODE0
10P (MSB / LSB)
MODE<1..0>\I

LAYER_UP2
LAYER_UP1
LAYER_UP0
9P (MSB / LSB)
LAY<2..0>\I

GO — GO\I
ENB — ENB\I
DTACK — DTACK\I
DONE_UP — DONE\I
BERR — BERR\I
AMCLK — VMECLK\I

ADD_UP13
ADD_UP12
ADD_UP11
ADD_UP10
ADD_UP9
ADD_UP8
ADD_UP7
ADD_UP6
ADD_UP5
ADD_UP4
ADD_UP3
ADD_UP2
ADD_UP1
ADD_UP0
8P (MSB / LSB)
7P (MSB / LSB)
6P ADD_UP<13..0>\I

DRAWING

TITLE=VME_CHIP
ABBREV=VMC
LAST_MODIFIED=Fri Jul 25 14:54:07 1997