DRAWING
VME_BUS
VME_BUS
LAST_MODIFIED=Fri Aug 7 09:06:55 1998

J2
CONVME64X
P2

IVADD<31..24>\I
ADVME<31..24>\I
ADVME<23..1>\I
IVADD<31..1>\I
IVADD<23..16>\I
ADVME<23..16>\I
ADVME<15..8>\I
IVADD<15..8>\I
ADVME<7..1>\I
IVADD<7..1>\I
VMEDAT<31..16>\I
VMEDAT<23..16>\I
VMEDAT<15..0>\I
VMEDAT<31..24>\I
WRITEVMEI_\I
ABTEOE_\I
DATA_TRANS
WRBRD_\I
LERDBRD_\I
RDBRD_\I
IVDATA<31..0>\I
DAT<31..0>

U72 U73 U74 U68 U69 U70 U71 U223 U224 U225
SN74ABTE 16245
TS
DIR1 OE
VCCBIAS DIR2
VPC

CONVME_5ROWS
VADD<23..1>
VMEDAT<15..0>
AM<5..0>
LWORD_
AS
DS1
DS0
WRITEVME_
IACK_
DTACK_
BERR_
FREEZE_P2\I
INIT_P2\I
AM<5..0>\I
LWORD_\I
AS\I
DS1\I
DS0\I
WRITEVME_\I
IACK_\I
IACKI_\I
ASI_\I
LWORDI_\I
AMI<5..0>\I
FREEZE\I
INIT\I
DTACK_\I
BERR_\I
IDTACK\I
IBERR\I
ERROR_\I
VME_GEAD_<4..0>\I
GA_<4..0>
+5V
VCC
AL502
SOIC
68PF
50V
1206
5A
AXIAL
SICTE-5

IC60
IC59
IC57
IC56
SOIC
FCT543
EN1
EN2