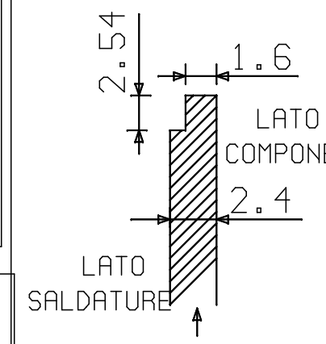
ESEGUIRE LE FRESATURE
SUL LATO SALDATURE
PER TUTTA LA LUNGHEZZA
DEI BORDI INDICATI
CON L´ASTERISCO *.



CIRCUITO
STAMPATO

366.7

D3
Gnd
Out Spy Buffer
U93
Hit Spy buffer
HBSPYBUF
XU10
Gnd
Vcc
UB71
UA71
U84
/Dtack
U86
U89
U90
U87
DS1
DS0
AS→
Geogr. Address
Warning:
Put Switches to
Vcc if VIPA crate
U68
U67
U65
U64
U66
3F1C 3F0C 2F1C 2F0C 1F1C 1F0C
4F1B 4F0B 3F1B 3F0B FSB 2F1B 2F0B 1F1B 1F0B
FSA 2F1A 2F0A 1F1A 1F0A
4F1D 4F0D 3F1D 3F0D FSD 2F1D 2F0D 1F1D 1F0D
U58 U59 U60 U55 U56 U57
U91 U80 U25 U24 U22 U20 U23 U21 U19
D4 D7
J3
Out
OUT4 XU7
OUT3 XU8
OUT2 XU6
OUT1 XU5
OUT0 XU4
Hit List Memory
MLDATA
XU2
U28 U29 U30
Gnd
TAG_RAM
U50 U54 U49 U53 U48 U52 U47 U51
NLAST2
XU17
TAG_CTR
XU16
VMEPLD
XU12
Fuse 10 A
U83
U95
U88
U85
P2
P1
P0
P3
U97 U79 U14 U98 U10 U5 U2 U9 U8 U1
D2 D5
J1
Road
OUTEN
XU3
ENMAP
XU9
Hold
NLay2
NLay1
NLay0
Enabled
Disabled
ON
HB_CTRFF
XU1
FIFO
ROAD
U16
U15
DLK00 XU13
DLK01 XU14
DLK02 XU15
DLK03 XU18
UB72
UA72
Eprom
U73
U82 U78 U96 U13 U12 U7 U4 U11 U6 U3
D1 D6
J2
Hit
D9
U94 U81
HRVME_NN
XU11
U18
U17
FIFO
HIT
SS+AM_MAP

U26 SS U27	U35 AM1 U36	U44 AM3 U43
U31 AM0 U32	U42 AM6 U41	U38 AM5 U37
U39 AM2 U40	U34 AM4 U33	U46 AM7 U45

Dnclk Clk Nclk

SVT - Hit Buffer
Infn - Pisa - CDF
s/n [redacted]

ECLIPSE

Eclipse-ITALY
HitBuf
SERIGRAFIA SUPERIORE

400

dim. 400 x 366.7