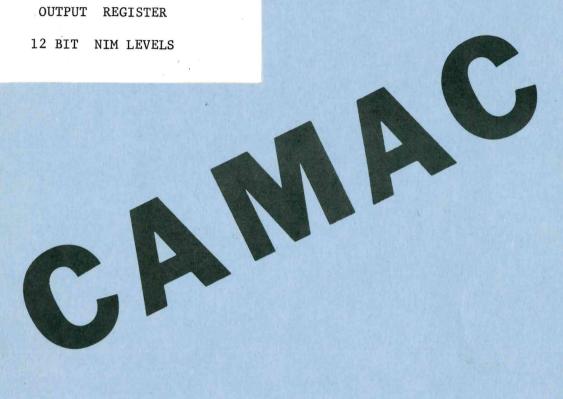
MODEL 41

OUTPUT REGISTER



INSTRUMENTATION FOR SCIENCE AND INDUSTRY MODEL 41

OUTPUT REGISTER

12 BIT NIM LEVELS

MANUAL FOR

SERIAL NO. 336

STANDARD WARRANTY

Jorway Corporation, warrants to the original purchaser that equipment of its manufacture is free from defects in material and workmanship. This warranty is effective for a period of one year after shipment of the instrument to the original purchaser.

Liability under this warranty is limited to servicing or adjusting any instrument returned to the Jorway Corporation factory for this purpose. Equipment to be returned for repair under warranty shall be returned transportation charges prepaid. If factory inspection discloses that defect developed under normal and proper use and within the warranty period, repair will be made and equipment returned with transportation charges paid by Jorway. Return shipment will be made via UPS where available or via insured parcel post. Other modes of shipment can be made at the customers request and then only on a collect basis.

If equipment is not covered under the warranty terms, an estimated cost of repair will be submitted to the customer, and upon his authorization, repairs will be accomplished and billed at cost, including U.S.Customs clearance charges and return insured transportation charges, F.O.B. Westbury, New York, U.S.A.

Equipment sold outside the U.S.A. will not be accepted for warranty repair unless prior approval and shipping instructions have been obtained from the Jorway Corporation. If equipment is covered under warranty, shipments will be returned prepaid to the foreign port of entry.

This warranty is expressly in lieu of all other obligations or liabilities on the part of Jorway Corporation. Jorway Corporation does not assume, nor authorize any other person, including representatives of Jorway Corporation to assume for them, any other liabilities in connection with the sale of Jorway instruments.



MODEL 41 OUTPUT REGISTER

The Model 41 12 bit Output Register is a single width Camac compatable module that meets the requirements of EUR4100e. The Model 41 provides up to 12 bits of information to be extracted from the Dataway Write lines. This information is stored and is available at the front panel as fast N.I.M. level signals, or optionally as power drivers. A versitile system of updating the storage registers is offered to provide flexibility of use. Data can be strobed into storage from the write lines by function command F16 or F17. On command F16, data is strobed in at S1 providing D.C. outputs. When pulsed outputs are required, function F17 is used. In response to F17·S1 data will be strobed into storage; at S2, however, all bits in the register will be reset to zero. Each time the register is updated a strobe output signal is generated. In response to F16·S1 it is a 3 μ sec. pulse. The strobe signal generated in response to F17·S1 will last from time S1 to S2, as do the data outputs.

In addition to these modes of operation the register may also be selectively set and reset. In this mode the Dataway Write lines are used as enable lines which select the bits that are to respond to the function requested. Function F20 will set to "1" any bit that has its write input at a logic "1". Function F22 will reset to "0" any bit that has its write input at a logic "1". Both F20 and F22 use strobe S1 to perform the operation.

An N light is provided to visually indicate when the module is addressed.

MODEL 41 SPECIFICATIONS

OUTPUTS: DATA and STROBE

Fast N.I.M. outputs, current source Logic "0" I out = 0 Logic "1" I out =-14ma min. Max. output voltage -2v

Pulse Width:

In response to F16, D.C. Level output In response to F17, P.W. is time from S1 to S2.

Option 1. Voltage Outputs, Open Collector Logic "0" +100v max. Logic "1" +.5v max. Max. power out 250 mw



MODEL 41 SPECIFICATIONS - Continued

DATAWAY COMMANDS (all fully decoded)

N•F16•AO•S1 Overwrites 12 bit storage register with data from write lines W1-W12, and

generates a strobe output signal.

N•F17•AO•S1 Same as N•F16•AO•S1

N•F17•AO•S2 Resets register and strobe output to zero.

N•F20•AO•S1 Sets to "1" the bits of the storage register that have their Write input at logic

"1".

N·F22·AO·S1 Resets to "0" the bits of the storage register that have their Write lines at a

logic "1".

DATAWAY CONTROLS

Z·S2 Resets storage register and strobe output to zero.

Q A'"Q" signal is generated in response to commands N•(F16+F17+F20+F22)

•A0

INDICATORS An "N" light is provided to visually indicate the module is addressed. The N

signal is integrated so that it will respond to a pulse.

POWER REQUIREMENTS +6v 420 ma nominal

-6v 200 ma nominal

TEMPERATURE RANGE 0°C to 60°C

SIZE Single Width Camac Module with protective side shields. Output connectors

are Lemo RA 00250 or equivalent.

SECTION V

SERVICE

5.1 GENERAL

Jorway instruments are conservatively designed using quality components with high reliability as a prime objective. In addition all units are manufactures to rigid quality control standards and are thoroughly tested and run before shipment. As a result long life and trouble-free operation can be expected. Nevertheless occasional malfunctions may occur necessitating service of the unit.

Most Jorway products are covered by our standard one year warranty. Instruments requiring service within the warranty period should be returned to the factory for repair under its terms (see warranty printed elsewhere in this manual). We also strongly recommend that the instruments which are out of warranty be returned to the factory when service is necessary. Jorway personnel being intimately familiar with our products are able to efficiently isolate any problem and restore the unit to proper operating condition.

If, for any reason, the user decides to repair an instrument it should be done by qualified personell experienced in working with this type of equipment. The information contained in this manual should be read and understood before attempting to troubleshoot the unit, since familiarity with the principles of operation and circuitry are essential. Utilizing the schematic diagrams and other material in the manual it should be possible for a skilled technician to determine the cause of a malfunction and effect its repair.

5.2 COMPONENT REPLACEMENT

If it is necessary to replace a component on the circuit boards, extreme care should be exercized so that the board is not damaged. Most important is that excessive heat is not applied to the printed circuitry. Use the minimum amount of heat necessary to melt the solder. Multilead components such as dual in-line integrated circuits are difficult to remove and it is usually better to cut the leads of suspected components and remove the remaining pieces one by one from the component holes. The use of a suitable solder extracting device will greatly aid in the removal of such components. The holes should be thoroughly cleaned out before new component is inserted. Do not use force in either removal or insertion of components as damage to the plated through holes may result.

SECTION VI

OPTIONS

No options other than those user selectable features described in Section II are available for this module at this time.

SECTION VII

PARTS LISTS

7.0 INTRODUCTION

The following lists of replaceable parts are included to permit repairs if it should become necessary. The first sheet shows the final assembly list which includes all major subassemblies. Following are parts lists for the subassemblies. Reference designations for electrical components correspond to those shown on the applicable schematic diagram. In some cases components of a different manufacturer may be used in some instruments, or a part with equivalent performance substituted.

Many components are readily available locally, however if difficulties in securing needed parts are experienced, they may be ordered from the factory using the Jorway stock number. Please include the model and serial number as shown on the nameplate of the instrument when ordering parts. Equivalent parts may be substituted in many instances. If in doubt about a particular replacement, our engineering department should be consulted.

arts List 41-A-701 Rev.F Assembly No. litle: Printed Circuit Board Reference Drawings: 41-J-001 Last ECO 593

Fisc File "A5284B.LIS 27-AUG-87"

REFERENCE DESIGNATION	QUANTITY	PART NUMBER	DESCRIPTION Cap.,Ceramic,.001uf,10%,1KV,X5F	MFR. CRL	MFR.PART NUMBER CE102
16 1,2,4 THRU 15,17 THRU 20,24,25 C3,21,22	1 20	C-0102-000 C-0104-003	Cap., Ceramic, . 1uf, 20%, 50V, 25U		1C2575U104M050B
=1,2,4 THRU 15,17 THRU 20,24,25	3	C-0226-000	Cap., Tant., Dipped, 22uf, 20%, 15V	Kemet	T368B226M015AS
	1	CR-4001-000	Diode, Silicon, 1A, 50V PRV	Mot	1N4001
R1	12	IC-7400-030	Integrated Circuit, DIP, Plastic	TI	SN7400N
C21,23,24,26,27,29,30,32,33,35,36,38	2	IC-7402-000	Integrated Circuit, DIP, Plastic	TI	SN7 402N
105,8	6	IC-7404-000	Integrated Circuit, DIP, Plastic	TI	SN7404N
C2,9 THRU 13	1	IC-7404-000 IC-7408-000	Integrated Circuit, DIP, Plastic	TI	SN7409N
C1	1	IC-7408-000 IC-7420-000	Integrated Circuit, DIP, Plastic	TI	SN7420N .
Tc7	1	1C-7420-000 IC-7437-000	Integrated Circuit, DIP, Plastic	TI	SN7437N
106	1	IC-7474-000	Integrated Circuit, DIP, Plastic	TI	SN7474N
1C6 C22,25,28,31,34,37 C3	5	IC-0121-000	Integrated Circuit, DIP, Plastic	TI	SN74121N
	4		Integrated Circuit, DIP, Plastic	TI	SN74122N
IC14	1	IC-0122-000	Integrated Circuit, DIP, Plastic	TI	SN74155N
£4 1,2	1	IC-0155-000	Inductor, Molded, . 47uh		4425-2M
	2	L-0474-000		FSC	PN3641
01	1	Q-3641-000	Transistor, NPN	NSC	PN5134
2 THRU 30	29	Q-5134-000	Transistor,NPN Resistor.1/4W. 10 OHM 5%	AB	RC07
15,19,25,31,37,43,49,55,61,68,74,80,86	13	R-0100-101		AB	RC07
R16,28,26,32,38,44,50,56,62,69,75,81,87	13	R-0101-101	Resistor, 1/4W. 100 OHM 5%	AB	RC07
814,22,28,34,40,46,52,58,64,70,76,82,88 8 R1,3,12,13,18,21,24,27,30,33,36,39,42	13	R-0181-101	Resistor, 1/4W. 180 DHM 5%	AB	RC07
.8	1	R-0221-101	Resistor,1/4W. 220 OHM 5%	AB	RC07
R1,3,12,13,18,21,24,27,30,33,36,39,42	13	R-0102-101	Resistor,1/4W. 1K 5% Resistor.1/4W. 1K 5%	AB	RC 07
R45,48,51,54,57,60,63,65,67,72,73,78,79	13	R-0102-101	1122	AB	RC07
184,85	2	R-0102-101	Resistor, 1/4W. 1K 5%	AB	RC07
17,23,29,35,41,47,53,59,66,71,77,83	12	R-0132-101	Resistor,1/4W. 1.3K 5%		RC07
R11	1	R-0152-101	Resistor,1/4W. 1.5K 5%	AB	RC07
2 9,89	1	R-0182-101	Resistor,1/4W. 1.8K 5%	AB	
9,89	2	R-0222-101	Resistor,1/4W. 2.2K 5%	AB	RC07
R4,5,6	3	R-0472-101	Resistor,1/4W. 4.7K 5%	AB	RC07
10 7,10	2	R-0103-101	Resistor,1/4W. 10K 5%	AB	RC07
1 7,18	1	TB-0041-003	Printed Circuit Board		41-200 Rev. C
™ A	1 1	W-0004-040	Jumper,#24 Wht Teflon,0.400" leng	th Squire	s J 0.400 x 0.250 124
_E, Y	2	W-0004-050	Jumper,#24 Wht Teflon,.500" lengt		
	1,	W-0034-000	Jumper,#24 Wht Teflon,length as r	eqdJorway	none

Parts List 41-A-100 Title: Top A Rev.H Assembly No. A2094E April 2, 1985 Printed Thu Aug 27 09:00:00 1987 Page 1 Title: Top Assembly

Reference Drawings: 41-J-001 Last ECO 593 Disc File "A2094B.LIS 27-AUG-87"

	REFERENCE	DESIGNATION	QUANTITY	PART NUMBER	DESCRIPTION	MFR.	MFR.PART NUMBER
CR2			1	A-5284-005	P.C.Board Assembly	Jorway	A5284E,41-A-701F
CR2			1	CR-0000-109	Diode,Light Emitting,Red	6.I.	MV5075C
			1	H-0000-114	Jackscrew,4mm	Deutsch	F7500-4MM-B-4
	-		2	H-0000-148	Rail,Camac	PMF	3403 Clr.Alod.
			6	H-0000-230	Standoff,1/4"Hex x 5/16",4-40	RAF	032185-A
			2	H-0000-240	Lug, Solder, 7mm	Lemo	RA00.252
			8	H-0403-000	Screw, Pan Hd., Phillips, Stainless	Any	4-40 X 3/16"
			2	H-0405-000	Screw, Pan Hd., Phillips, Stainless	Any	4-48 x 3/8"
_			6	H-0403-001	Screw, Flat Hd., Phillips, Stainless	Any	4-40 x 3/15",100 deg
-0			6	H-0406-001	Screw, Flat Hd., Phillips, Stainless	Any	4-40 x 3/8"
			10	H-0401-300	Washer,Lock,Med.Split,Stainless	Any	#4
J1 THRU J1	3		13	J-0001-205	Connector, Coaxial	Lemo	RA00.250
_			1	MP-0000-113	FRONT PANEL, ENGRAVED	JORWAY	41-C-400
			2	MP-2257-000	Cover, Aluminum	Jorway	001-A-454

APPENDIX 1

TABLE A1-1 CONTACT ALLOCATION AT A NORMAL STATION

(Viewed from front of crate)

	9						
Bus-line		Free Bus-line		P1	В	Busy	Bus-line
Bus-line	100	Free Bus-line		P2	F16	Function	Bus-line
	al patch con			Р3	F8	Function	Bus-line
	al patch con			P4	F4	Function	Bus-line
	al patch con	tact		P5	F2	Function	Bus-line
Bus-line		Command Accep	pted	X	F1	Function	Bus-line
Bus-line		Inhibit		i	A8	Sub-address	Bus-line
Bus-line	1	Clear		С	A4	Sub-address	Bus-line
Individua	l line	Station Number	a.	N	A2	Sub-address	Bus-line
Individua	l line	Look-at-Me		L	A1	Sub-address	Bus-line
Bus-line		Strobe 1		S1	z	Initialise	Bus-line
Bus-line		Strobe 2		S2	a	Response	Bus-line
		35	(W24	W23		2000
		Ψ,		W22	W21		
			- 1	W20	W19		,
			-	W18	W17		
24 V	Nrite Bus-lin	es		W16	W15		
)	W14	W13		
	= least sign		1	W12	W11		
W24	= most sigr	nificant bit		W10	W9		
				W8	W7		
				W6	W5		
			- 1	W4	W3		
			Ĺ	W2	W1		,
			C	R24	R23		
				R22	R21		
			- 1	R20	R19		
				R18	R17		
24 R	Read Bus-line	হ		R16	R15		
				R14	R13		
R1	= least signi	ficant bit)	R12	R11		
R24	= most sign	ificant bit		R10	R9		
				R8	R7		
				R6	R5		
				R4	R3		
			Ĺ	R2	R1		
	٠ -	12V d.c.		-12	-24	-24V d.c.	.)
	+	200V d.c.		+200	-6	-6V d.c.	
Power	1	17V a.c. Live		ACL	ACN	117V a.c. Neutral	Power
Bus-lines	-{ F	Reserved		Y1	E	Clean Earth	Bus-lines
	+	12V d.c.		+12	+24	+24V d.c.	
	F	Reserved		Y2	+6	+6V d.c.	
	(0	V (Power Return)		0	0	OV (Power Return)	J
							Ned.

TABLE A1-2 CONTACT ALLOCATION AT THE CONTROL STATION

(Viewed from front of crate)

Bus-line	Individual p Individual p Individual p Individual p Bus-line Bus-line Bus-line Individual p	patch contact patch contact patch contact patch contact patch contact patch contact	Command Accepted Inhibit Clear	P1 P2 P3 P4 P5 X I C P6	B F16 F8 F4 F2 F1 A8 A4 A2 A1		Busy Function Function Function Function Function Sub-address Sub-address Sub-address	Bus Bus Bus Bus Bus Bus Bus	s-line s-line s-line s-line s-line s-line s-line s-line	
Company				S1			nitialise	_		
L23	Dus line		Strobe 2			۱ '	nesponse	bus	-IIIIe	
L22)					
L20										
L19 N19 L18 N18 L17 N17 L16 N16 L15 N15 L14 N14 L13 N13 24 individual Station Number lines L1 from Station 1, etc. L12 N12 L11 N11 L10 N10 L9 N9 L8 N8 L7 N7 L6 N6 L5 N5 L4 N4 L3 N3 L2 N2 L1 N1 L1 N1 L3 N3 L2 N2 L1 N1 L1 N1 L1 N1 L3 N3 L2 N2 L4 N4 L3 N3 L4 N4 L3 N3 L4 N4 L3 N3 L4 N4 L3 N3 L4 N4 L4 N4 L5 N6 L5 N5 L4 N4 L5 N6 L5 N5 L6 N6 L5 N				1	N21					
L18				L20	N20					
L17				L19	N19					
L16				1						
L15										
L14										
24 individual Look-at-Me lines L1 from Station 1, etc. L13				1						
L11 from Station 1, etc. L12	24 :	lividual Laalvak A	An liman			1 .	M: ": 1 . 1 . C			
L11						_				
$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$	Liffo	om Station 1, etc.		10000 00000		"	vi to Station I, etc.			
$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$			a ·							
$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$						1				
$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$										
$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$			vi	1000000			41			
				1						
				L4						
				L3	N3					
$ \begin{cases} -12 \ d.c. & -12 \ -24 \ -24 \ $				L2	N2					
+200V d.c. +200 -6 -6V d.c. Power Bus-lines				L1	N1 _	J				
Power 117V a.c. Live ACL ACN 117V a.c. Neutral Power Bus-lines Reserved Y1 E Clean Earth Bus-lines +12V d.c. +12 +24 +24V d.c. Reserved Y2 +6 +6V d.c.		-12V d	.c.	-12	-24	_	24V d.c.)		
Bus-lines Reserved Y1 E Clean Earth Bus-lines +12 V d.c. +12 +24 +24 V d.c. Reserved Y2 +6 +6 V d.c.		+200V	d.c.	+200	-6	-	6V d.c.			
+12V d.c. +12 +24 +24V d.c. Reserved Y2 +6 +6V d.c.	Power	117V a.	c. Live	ACL	ACN	1	17V a.c. Neutral		Power	
Reserved Y2 +6 +6V d.c.	Bus-lines					С	lean Earth	>	Bus-lines	
							757 15			
OV (Power Return) 0 0 OV (Power Return)		K SUPERING SOUR								
		OV (Pov	ver Return)	0	0	0	V (Power Return)	J		

SECTION VIII

DRAWINGS

8.0 INTRODUCTION

The following drawings are included as a reference for those reading the principles of operation text presented in Section IV, and as an aid to personnel servicing module.

8.1 Model 41

12 Bit Output Register Component Layout 41-D-350

12 Bit Output Register Schematic 41-J-001